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MOPMB011 Deposition and Characterisation of V3Si films for SRF Applications

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Introduction

A15 materials are potential alternatives to Nb for next generation thin film SRF cavities. V₃Si is a promising candidate due to their high critical temperature (17 K) and upper critical field (24.5 T)¹.

V₂Si films have been manufactured by a variety of methods: Thermal diffusion, co-sputtering and single alloy targets, all with varying levels of success. Film quality, surface roughness, V-Si phases and other defects all hinder cavity performance.

Presented is the progress made on planar V₃Si thin films and initial results of a V₃Si split 6 GHz cavity at Daresbury Laboratory, UK.

V₃Si thin films on planar samples





Deposition Parameters	Units		
Voltage	[V]	566	Table 1: Deposition parameters and substrate temperatures for all depositions shown.
Current	[A]	0.53	
Power	[W]	300	
Frequency	[kHz]	350	
Pulse Length	[µs]	1.1	
Deposition Length	[min]	180	
S1 Substrate Temperature	[°C]	670	
S2 Substrate Temperature	[°C]	710	

Planar: results and discussion

VSi films were deposited on two 50 mm oxygen free high conductivity (OFHC) copper disks, using pulsed DC magnetron sputtering, labelled S1 and S2. The substrate temperature varied between the two samples.

Secondary electron microscopy of the surfaces (fig. 2 and fig. 3) show a significant variation in grain size, structure and uniformity. Energy dispersive X-ray spectroscopy (EDX) results were:

S1 - V : 71% and Si : 29%

S2 - V : 76% and Si : 24%

Both samples were tested using The Magnetic Penetration Facility (MPF)² at Daresbury Laboratory, where a significant variation in superconductivity between the two samples was observed.

Figure 1: Photograph of a V₂Si thin film coated planar Cu sample

S1



Figure 4: DC magnetic penetration measurements at 4.2 K for both S1 and S2 samples. First field penetration (B_{fn}) is extracted at three points.



Figure 2: Secondary electron microscope image of a V₃Si thin film coated planar Cu sample (S1). Displays a grain structure with grain sizes of a few 100 nms.



Figure 5: Extracted B_{fn} as a function of sample temperature for S1 using the DC magnetic penetration measurements



Figure 4 - shows the variation between the two samples at 4.2 K with the complete field penetration measurements as a function of temperature in figures 5 and 6.

> Figure 3: Secondary electron microscope image of a V₃Si thin film coated planar Cu sample (S2). The increased temperature compared to previous sample, S1, led to significant increase in grain size.

Figure 6: Extracted B_{fn} as a function of sample temperature for S2 using the DC magnetic penetration measurements

V₃Si thin film on a split 6 GHz cavity







Figure 7: Photograph of the split 6 GHz cavity during depositon

Figure 8: Photograph of the split 6 GHz cavity after deposition

Figure 9: The surface resistance as a function of temperature for Nb and VSi coated split cavity

Conclusion

This work highlights the promise of V₃Si as a potential alternative to Nb thin films for SRF applications. Planar depositons results show high performance in DC conditions and further improvements through post processing and HiPIMS may result in more uniform and stiochiometric films.

First RF tests of V₃Si at Daresbury laboratory show a Rs a factor of 2 higher than the Nb deposition. However, improvements in the cavity heating stagae and chemical polishing steps are being established which will reduce residual resistance of the cavity.

Split 6 GHz Cavity

Nb and then V₃Si was deposited using the the parameters listed in Table. 1. Cavity temperature was 400 C and 530 C during deposition for Nb and V₃Si respectively.

The cavity was tested at Daresbury laboratory in a low power RF facility measuring surface resistance (R₂)³. A superconducting transition is observed. However the results are both dominated by residual resistance of the system. The cavity underwent mechanical finishing but no polishing techniques would could explain the high R₂. Further work is required.

References and Acknowledgements

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